

Bounded context switching for valence systems

Roland Meyer¹, **Sebastian Muskalla**¹, and Georg Zetsche²

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- 1 TU Braunschweig, Germany
`{roland.meyer,s.muskalla}@tu-bs.de`
- 2 IRIF (Université Paris-Diderot, CNRS), France
`zetsche@irif.fr`

Theorem

Reachability under *bounded context switching*
for *valence systems over graph monoids*
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3. What is *BCS for valence systems*?

1. BCS

The problem

Setting:

Concurrent system, each component modeled as automaton

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Solution:

Consider bounded context switching (BCS)

Bounded context switching

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Given: Concurrent system \mathcal{S} , number k (in unary)

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Useful as bugs *usually* occur within few context switches
[MQ07,LPSZ08]

Example

Example [QR05]:

Concurrent system where each component is a PDS,
communicating via finite control

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Reachability under BCS is **NP-complete**

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- various types of components,
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For example:

Queues as storages [LMP08]

Pushdowns with dynamic thread creation [ABQ09]

Pushdowns communicating via queues [HLMS12]

...

Our goal: General BCS result

Related work II

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Using **graph-theoretic measures** (tree width, ...) [MP11, A14]

- Can handle queues
- Cannot handle counters
- Applies to settings where the complexity is beyond NP

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Our technique provides an **algebraic view**

2. Valence systems

Valence systems

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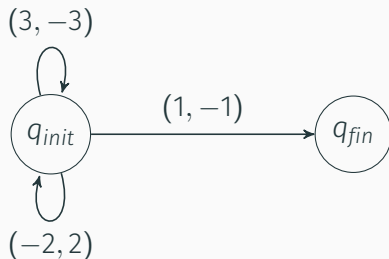
Semantics:

- Configurations** (q, m) with q control state, $m \in \mathbb{M}$

- Transition** $q \xrightarrow{m'} q'$ leads to $(q', m \cdot m')$

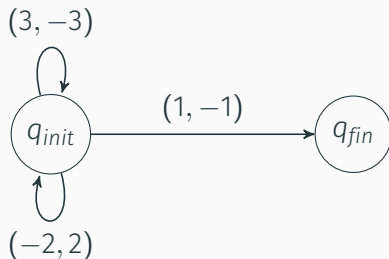
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Valence system over $\mathbb{Z} \times \mathbb{Z}$ (with component-wise addition)



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(essentially an integer 2-VASS)

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Theorem

*If monoid \mathbb{M} satisfies condition c , then checking property P for *all* valence systems over \mathbb{M} is in complexity class \mathcal{C} .*

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Graph monoids

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For example, want classification of $P = \text{reachability}$

Reachability for valence systems

Given: Valence system \mathcal{A} over monoid \mathbb{M}

Decide: $(q_{init}, 1_{\mathbb{M}}) \rightarrow^* (q_{final}, 1_{\mathbb{M}})?$

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Monoid elements: Sequences of operations
modulo the **congruence** $o^+.o^- \cong \varepsilon$

Example: PDS

•
a

•
b

$$\mathbb{M}_G = \{a^+, b^+, a^-, b^-\}^* / \cong$$

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Valence systems over \mathbb{M}_G are PDS over stack alphabet $\{a, b\}$

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If $o \mathcal{I} u$, then o^\pm and u^\pm commute: $o^\pm.u^\pm \cong u^\pm.o^\pm$

Example: VASS



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$$o^+.o^- \cong \varepsilon \quad \forall o \in \{a, b\}$$

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Valence systems over \mathbb{M}_G are 2-VASS

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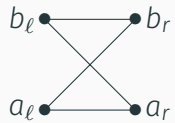
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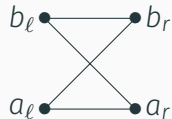
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Valence systems over \mathbb{M}_G are **integer 2-VASS**

Example: MPDS



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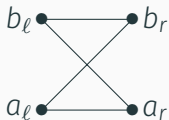


Any $m \in \{a_l^+, a_l^-, \dots\}^*$ can be written as

$$m \cong m_{\upharpoonright_{a_l}} \cdot m_{\upharpoonright_{a_r}}$$

such that $m \cong \varepsilon$ iff $m_{\upharpoonright_{a_l}} \cong \varepsilon$ and $m_{\upharpoonright_{a_r}} \cong \varepsilon$

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Valence systems over \mathbb{M}_G are **2-PDS** (with a binary stack alphabet for each stack)

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Graph monoids cannot model:

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Higher-order stacks

Characterization results for **valence systems/automata**:

reachability [Z15]

regularity [Z11]

context-freeness [BZ13]

semilinearity of the Parikh image [BZ13]

...

3. BCS for valence systems

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The components share a control state

(communication between components)

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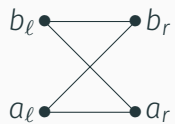
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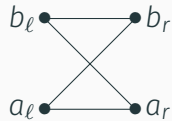
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Crucial as our notion of context is not invariant under congruence

Contexts

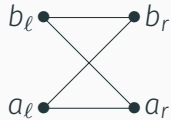


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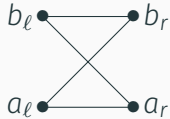
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Intuitively:

$$m = \dots o^\pm . u^\pm \dots$$

with $o \mathcal{I} u$, then this constitutes a **context switch**

Contexts



Nodes belonging to independent parts of the storage are connected by an edge

Intuitively:

$$m = \dots o^\pm . u^\pm \dots$$

with $o \mathcal{I} u$, then this constitutes a **context switch**

In general, we need a more restrictive definition

Dependent computations

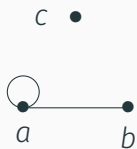
Definition

A sequence of operations m is called **dependent** if for all o^\pm, u^\pm in m with $o \neq u$, $o \mathcal{I} u$ **does not hold**.

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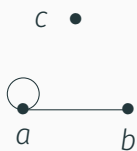
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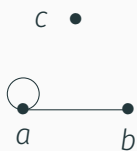
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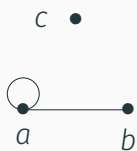


a^+c^+	dependent
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$a^+c^+b^+$	not dependent
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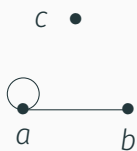


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a^+a^-	dependent
$a^+b^+b^-a^-$	not dependent
	but $a^+a^- \cong a^+b^+b^-a^-$!

Contexts & context switches

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The i^{th} **context** of m is the maximal dependent prefix of m with the first $i - 1$ contexts removed

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The number of **context switches** $cs(m)$ is the number of contexts minus 1

In the examples

Assume the number of context switches is bounded by k



PDS

no restriction



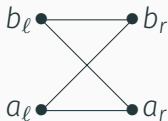
VASS

changing the counter $\leq k$ times



integer
VASS

changing the counter $\leq k$ times



MPDS

changing the stack $\leq k$ times

BCSREACH for valence systems over graph monoids

Given: Valence system \mathcal{A} over \mathbb{M}_G , number k (in unary)

Decide: Is there $(q_{init}, \varepsilon) \rightarrow (q_{final}, m)$
with $m \cong \varepsilon$ and $cs(m) \leq k$?

The result

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Theorem

BCSREACH for valence systems over graph monoids is in NP (for all graph monoids).

The proof / The algorithm

Proof outline

Need to find a computation $(q_{init}, \varepsilon) \rightarrow^* (q_{final}, m)$ with $m \cong \varepsilon$

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Ensure irreducibility by saturating system

Then check existence of reducible block decomposition using guessing and representing blocks as **finite automata**

Block decomposition

If $m \cong \varepsilon$, then there is a **reduction** of m that
swaps letters
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E.g. $m_1.m_2 \rightarrow m_2.m_1$ if **every symbol** in m_1 commutes with **every symbol** in m_2

Block decomposition

Let $m = m_1, m_2, \dots, m_n$ be a decomposition of m into blocks.

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Coarser decompositions might not be freely reducible:

$$o^+u^+, u^-, o^-$$

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Theorem

Let m be a sequence of operations with

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each of them irreducible, and

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Size of the decomposition is **independent** of the length of m
Existence can be **checked algorithmically**

The algorithm, Step I

The algorithm

Given: valence system \mathcal{A} , bound k

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Part I: Enforcing irreducibility

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Part I: Enforcing irreducibility

1. Guess $\leq k$ **dependent parts** of \mathcal{A}

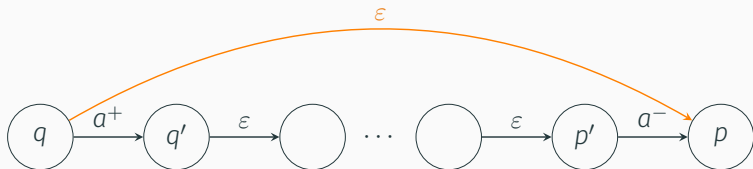
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Part I: Enforcing irreducibility

1. Guess $\leq k$ **dependent parts** of \mathcal{A}
2. Saturate each part:



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Let \mathcal{A}_{sat} be the resulting valence system

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$(q_{\text{init}}, \varepsilon) \rightarrow^* (q_{\text{final}}, m)$ in \mathcal{A} with $m \cong \varepsilon$ and $cs(m) \leq k$,

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Let \mathcal{A}_{sat} be the resulting valence system

Theorem

$(q_{\text{init}}, \varepsilon) \rightarrow^* (q_{\text{final}}, m)$ in \mathcal{A} with $m \cong \varepsilon$ and $\text{cs}(m) \leq k$,

iff

$(q_{\text{init}}, \varepsilon) \rightarrow^* (q_{\text{final}}, m')$ in \mathcal{A}_{sat} with $m' \cong \varepsilon$, $\text{cs}(m') \leq k$,
and *contexts of m' irreducible*.

The algorithm, Step II

Part II:

Checking the existence of a freely reducible block decomposition

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3. For each context i , guess part of \mathcal{A}_{sat} that is used in block $m_{i,j}$ as NFA $\mathcal{A}_{i,j}$

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Cancel rule applicable to $\mathcal{A}_{i,j}, \mathcal{A}_{i',j'}$
if $\mathcal{L}(\mathcal{A}_{i,j}) \cap \mathcal{L}(\mathcal{A}_{i',j'})^{\text{inverse}}$ is non-empty

Complexity for fixed graphs

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Now, assume that the graph G is *fixed*, consider $\text{BCSREACH}(G)$

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Theorem

If G^- is a *clique*, then $\text{BCSREACH}(G)$ is *NL-complete*.

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P_4



C_4

=



Complexity for fixed graphs

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Theorem

If G^- **contains** C_4 as induced subgraph,
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Theorem

If G^- **contains** C_4 as induced subgraph,
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Theorem

If G^- **contains neither** C_4 nor P_4 as induced subgraphs,
then $\text{BCSREACH}(G)$ is in **P**.

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Reachability under *bounded context switching*
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Richer model supporting queues, higher order?

Thank you!

Questions?